

AMENDMENTS TO THE SPECIFICATION

Please amend paragraph number 0084 as follows:

**[0084]** The full disclosures of the following copending U.S. patent applications are hereby incorporated into the present application by reference: U.S. Patent Application No. 09/231,998, filed January 15, 1999, now abandoned, entitled “STORAGE CELLS UTILIZING REDUCED PASS GATE VOLTAGES FOR READ AND WRITE OPERATIONS”, and commonly assigned herewith; U.S. Patent Application No. 09/281,008, filed March 30, 1999, now issued as United States Patent Number 6,301,696 entitled “METHOD AND APPARATUS FOR INCREASING THE SPEED OF FIELD-PROGRAMMABLE GATE ARRAYS”, and commonly assigned herewith; U.S. Patent Application No. 09/285,563, filed April 2, 1999, now issued as United States Patent Number 6,446,242, entitled “METHOD AND APPARATUS FOR STORING A VALIDATION NUMBER IN A FIELD-PROGRAMMABLE GATE ARRAY”, and commonly assigned herewith; and U.S. Patent Application No. 09/318,198, filed May 25, 1999, now issued as United States Patent Number ~~6,211,691~~ 6,211,697 entitled “INTEGRATED CIRCUIT THAT INCLUDES A FIELD-PROGRAMMABLE GATE ARRAY AND A HARD GATE ARRAY HAVING THE SAME UNDERLYING STRUCTURE”, and commonly assigned herewith.